## **WINCOM TECH**

# PRODUCT SPECIFICATION FOR LCD MODULE

**Revision:** 0.0

**Model No:** <u>T20B01</u>

**Module Type:** COG+FPC+B/L

APPROVED	SIGNATURE	

- □ Approved Product Specification only
- Approved Product Specification and Samples

Prepared By	Checked By	Approved By

# WINCOM TECH

#### **Contents**

1. General Description	3
2. Physical Features	
3. Mechanical Specification	3
4. Outline Dimension	4
5. Absolute Maximum Ratings	5
6. Electrical Characteristics	5
7. Module Function Description	6
8. Backlight Characteristics	9
9. Electro-Optical Characteristics	10
10. Reliability	12
11. Inspection Standards	13
12. Precautions For Using LCD Modules	19
13. Records Of Version	21

### 1. General Description

T20B01 is a transmissive type a-Si TFT-LCD (amorphous silicon thin film transistor liquid crystal display) module, which is composed of a TFT-LCD panel, a driver circuit and a backlight unit. The panel size is 2.0 inch and the resolution is 176\*220, the panel can display up to 262K colors. The LCM can be easily accessed by micro-controller via parallel interface.

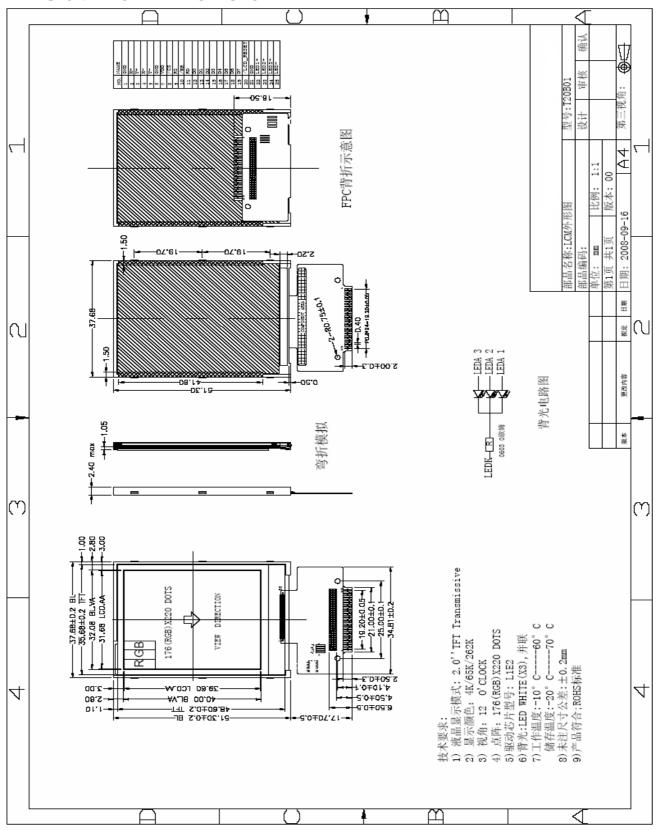
### 2. Physical Features

Display Mode	TFT-LCD Module
Display Mode	Active matrix TFT, Transmissive type
Display Format	Graphic 176×RGB×220 Dot-matrix
Input Data	8bit bi-direction parallel interface by 80 MPU
Viewing Direction	12 O'clock
Drive	L1E2

### 3. Mechanical Specification

Item	Contents	Unit
Module size (W×H×T)	37.68 × 51.30 × 2.4	mm
Number of dots	176(RGB) × 220	
Active area (W×H)	31.68 × 39.60	mm

### 4. Outline Dimension



### 5. Absolute Maximum Ratings

Item	Symbol	Min	Max	Unit	Remark
Power Voltage	VCC	-0.3	3.3	V	
Input Voltage	VIN	-0.3	VCC+0.3	V	Note1
Operating temperature	TOPR	-20	70	$^{\circ}$	Note2
Storage temperature	TSTR	-30	80	$^{\circ}\!\mathbb{C}$	
Humidity			90	%RH	

Remark:

Note 1) The L1E2 may be permanently damaged if it is used under the condition exceeding the above absolute maximum values. It is also recommended to use the L1E2 within the limit of its electric characteristics during normal operation. Exceeding the conditions may lead to malfunction of L1E2 and affect its credibility.

Note 2) The voltage from VSS.

#### 6. Electrical Characteristics

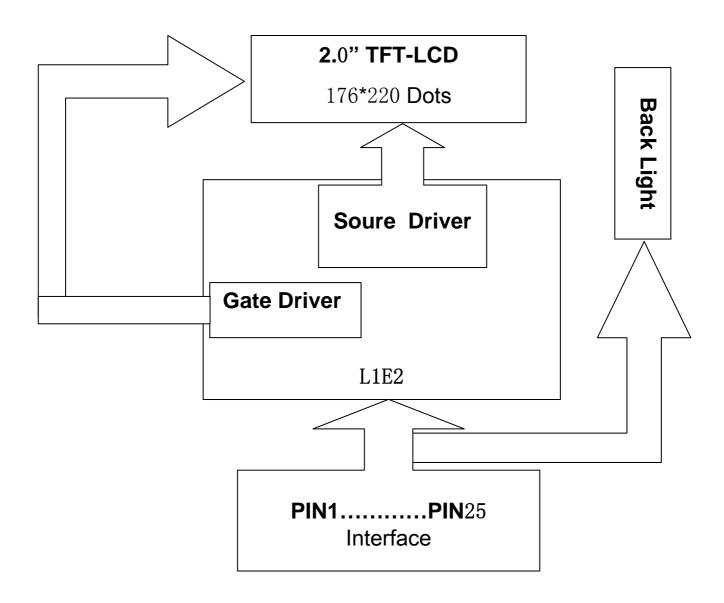
Item		Symbol	Symbol Rating				Remark
		Syllibol	Min	Тур	Max	Unit	Remark
Power Voltage	Logic	VCC	2.5	2.8	3.3	V	Note1
Input Voltage	L level	VIL	-0.3		0.2*VCC	V	VCC=2.5
input voltage	H level	VIH	0.8* VCC			V	~ 3.3V
LCD Drive P		ILCD			15	mA	

Remark:

Note1:Vcom must be adjusted to optimize display quality: Cross-talk, Contrast Ratio and etc.

### 7. Module Function Description

### 7-1. Block Diagram Of LCM

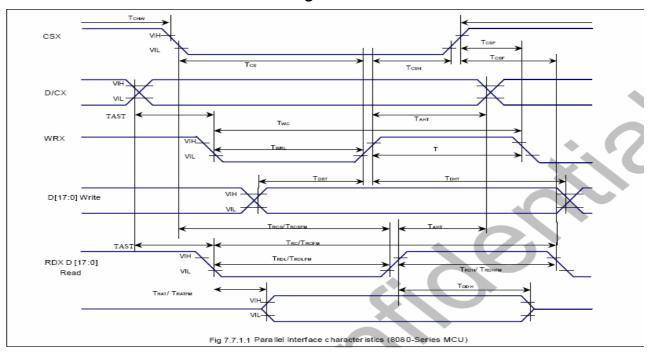


### 7-2. Pin Description

NO.	Symbol	Function
1	GND	System GND
2	X+	
3	Y+	Touch Panel Pad
4	X-	Touch Faller Fau
5	Y-	
6	GND	System GND
7	VDD	Power Supply
8	CS	Chip Select
9	RS	Command Or Data Select
10	WR	Write Enable
11	RD	Read Enable
12	DB0	
13	DB1	
14	DB2	
15	DB3	Input Or Output Data Bus
16	DB4	input Of Output Data Bus
17	DB5	
18	DB6	
19	DB7	
20	RESET	System Reset
21	GND	System GND
22	LED1+	
23	LED2	Anode of Backlight
24	LED3+	
25	K	Kathode Of Backlight

### 7-3. Timing Characteristics

#### 7.3.1 8080-series Interface Timing Characteristics



VCC=2.5~3.3V , Ta=25 °C

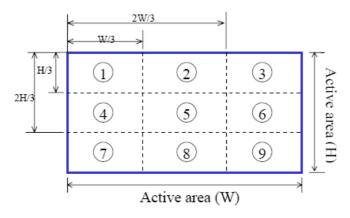
Signal	Symbol	Parameter	MIN	MAX	Unit	Description
D/CX	tAST	Address setup time	10		ns	
tAHT		Address hold time (Write/Read)	10		ns	-
	tCHW	Chip select "H"pulse width	0		ns	
	tCS	Chip select setup time (Write)	35		ns	
CSX	tRCS	Chip select setup time (Read ID)	45		ns	
COX	tRCSFM	Chip select setup time (Read FM)	355		ns	
	tCSF	Chip select wait time (Write/Read)	10		ns	
	tCSH	Chip select hold time	10		ns	
	tWC	Write cycle	80		ns	
WRX	tWRH	Control pulse "H" duration	35		ns	
	tWRL	Control pulse "L" duration	35		ns	
	tRC	Read cycle (ID)	160		ns	When read ID
RDX (ID)	tRDH	Control pulse "H" duration (ID)	90		ns	data
	tRDL	Control pulse "L" duration (ID)	45		ns	data
	tRCFM	Read cycle (FM)	450		ns	When read from
RDX (FM)	tRDHFM	Control pulse "H" duration (FM)	90		ns	frame memory
	tRDLFM	Control pulse "L" duration (FM)	355		ns	name memory
	tDST	Data setup time	10		ns	For maximum
	tDHT	Data hold time	10		ns	CL=30pF For
D[17:0]	tRAT	Read access time (ID)		40	ns	minimum
	tRATFM	Read access time (FM)		340	ns	CL=8pF
	tODH	Output disable time	20	80	ns	ос-орг

8. Backlight Characteristics

Item	Symbol	Min	Тур	Max	Unit	Remark
Forward voltage	VBL	-	3.3	-	V	-
Current	I <sub>BL</sub>	-	20	-	mA/chip	-
ICE	X	0.26	-	0.32	-	
ICE	Y	0.26	-	0.32	-	-
Brightness	-	2600	-	-	cd/m²	<b>★</b> 1
Uniformity	-	80	-	-	%	<b>★</b> 2

#### ★1 Test condition is:

- (a) Center point on active area
- (b) Best Contrast
- ★2 Uniform measure condition:
  - (1)Measure 9 point. Measure location is show below:
  - (2)Uniform = (Min. brightness / Max. brightness)×100%
  - (3)Best Contrast.

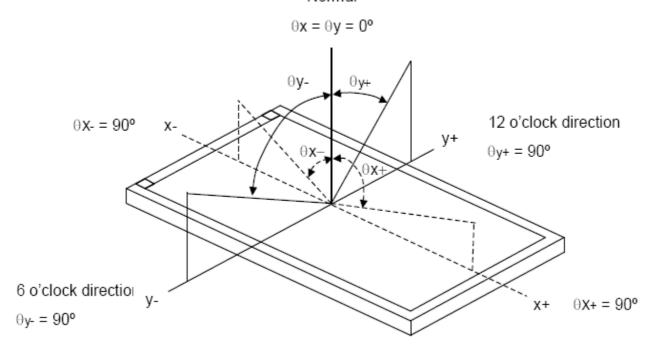


### 9. Electro-Optical Characteristics

Item		Symbol	Conditi on	Min.	Тур.	Max.	Unit	Remark
Response	time	Tr +Tf			50	70	ms	
Contrast R	Ratio	CR		150	250			
Transmitta	ince	Т%			6.3		%	
	white	Wx		0.264	0.294	0.324		
	wille	Wy	0	0.304	0.334	0364		
	Dod	Rx	$\theta x = \theta y$	0.603	0.633	0.663		
Color	Red	Ry		0.296	0.326	0.356		Reference
chromaticity	Gree	Gx		0.267	0.297	0.327	_	Only
	n	Gy		0.547	0.577	0.607		
	Blue	Вх		0.103	0.133	0.163		
	Diue	Ву		0.099	0.129	0.159		
	Hor.	θ <b>x</b> +			45	-		
Viewing	1101.	θx-	CR ≥ 10		45		Dea	
angle	Ver.	θу+	UK ≥ 10		35		Deg.	
	V CI.	θу-			15			

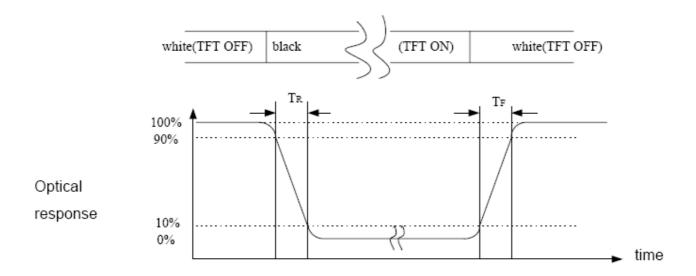
Note (1) Definition of Viewing Angle x and y:



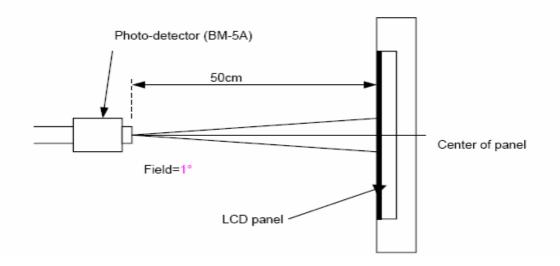


#### Note (2) Definition of Contrast Ratio(CR): measured at the center point of panel

Note (3) Definition of Response Time : Sum of  $T_R$  and  $T_F$ 



Note (4) Definition of optical measurement setup



### 10. Reliability

#### 10.1. MTBF

The LCD module shall be designed to meet a minimum MTBF value of 50000 hours with normal. (25°C in the room without sunlight)

#### 10. 2. Test condition

NO.	ITEM	CONDITION	CRITERION
1	High Temperature Non-Operating Test	80℃ * 240Hrs	<ul> <li>No Defect Of</li> <li>Operational Function In</li> <li>Room Temperature Are</li> </ul>
2	Low Temperature Non-Operating Test	-30°C * 240Hrs	Allowable.  IDD of LCM in Pre-and
3	High Temperature/Humidity Non-Operating Test	50℃ * 90%RH * 240 Hrs	Post-Test Should Follow Specification
4	High Temperature Operating Test	70℃ * 240Hrs	
5	Low Temperature Operating Test	-20°ℂ * 240Hrs	
6	Thermal Shock Test	-30°C (30Min )↔ 80(30Min)* 10 Cycles	

#### Notes:

- 1. Judgments should be made after exposure in room temperature for two hours.
- 2. The distill water is used for the high temperature / humidity test.
- 3. The sample above is individually for every reliability tests condition.

### 11. Inspection Standards

AQL(Acceptable Quality Level)
 AQL of major and minor defect

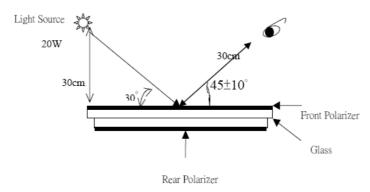
	MAJOR DEFECT	MINOR DEFECT	MAJOR+MINOR
APPEARANCE	0.40%	1.0%	1.0%
ELECTRIC-OPTICAL	0.15%	0.15%	0.15%

#### 2. Basic conditions for inspection

The LCM face to us, in normal environment, the lux is 1000±200.(Darkroom's lux:100±50),

About an angle of incidence 30, a distance of 30cm with normal eye,with an angle of 45 degree to check the products without uncovering the film!

(As shown below)



- 3. Inspection item and criteria
- 3.1 Visual inspection criterion in immobility

#### 3.1.1 Glass defect

No	Defect item	Criteria	Remark
	Dimension Unconformity	By Engineering Drawing	
1			
	(Major defect)		

No	Defect item	Criteria	Remark
2	Cracks (Major defect)	1.Linear cracks on panel	
3	Glass extrude the conductive area (minor defect)	a: disregards and no influence assemblage 1) b≤1/3Pin width(non bonding area)	a:Length, b:Width
4	Pin-side , conductive area damaged (minor defect)	(a $c: disregards$ ) $b \le 1/3$ of effective length for bonding electrode [Accept]	a: Length, b: Width, c: Thickness
5	area damaged (minor defect)	1) Damage area don't touch the ITO (Inclueling contraposition mark,except scribing mark)	a:Length, b:Width, c:Thickness

No	Defect item	Criteria		Remark
	Non-pin-side damage	c <t< td=""><td></td><td>c : Thickness b: width of</td></t<>		c : Thickness b: width of
		1) b exceeds 1/3 BM		damage
	(minor defect)			
6			[Reject]	BM內緣
		c=T		
		b not touch the seal glue		<b>→</b>    <b>←</b> ·
			[Reject]	

3.1.2 LCD appearance defect (View area)

No	Defect item	Criteria		Remark
	Fiber · glass	Specification	Allowable	note1: L:Length,W:Width
1	cratch · polarizer	0.05mm <w≦0.1mm;< td=""><td></td><td>note2: disregard if out of AA</td></w≦0.1mm;<>		note2: disregard if out of AA
'	scratch/folded	L≦3.0mm	1	L D
	(minor defect)	W>0.1mm ; L>3.0mm	0	
	Polarizer bubble \	ψ≦0.2mm	disregard	note 1:ψ=(L+W)/2; Length , W:
2	concave and convex	0.2mm<ψ ≦ 0.3mm	2	Width
-	(minor defect)	0.3mm<ψ ≦ 0.5mm	1	note2: disregard if out of AA
		0.5mm<ψ	0	
	Diagrapha distributate	ψ≦0.15mm	disregard	note2: disregard if out of AA
2	Black dots · dirty dots · impurities · eyewinker	0.15mm<ψ ≦ 0.25mm	2	$\bigcirc \qquad \boxed{\downarrow} \; \phi$
		0.25mm<ψ ≦ 0.3mm	1	<b>←→</b>
	(Major defect)	0.3mm<ψ	0	ψ
	Polarizer prick	ψ≦0.1mm	disregard	note1:ψ=(L+W)/2 ; L= Length ,
4	(Major defect)	0.1mm<ψ≦0.25mm	3	W=Width note2: the distance between two
		ψ>0.25mm	0	dots >5mm

15

#### 3.1.3 .FPC

	0.110			
No	Defect item	Criteria		Remark
1	Copper screen peel (Major defect)	Copper screen peel	[Reject]	
2	No release tape or peel (Major defect)	No release tape or peel	[Reject]	
	Dirty dot and impurity of	Specification	Allowable	note1: Cannot have stride ITO
	FPC for customer using	ψ≦0.25mm	2	impurities
	side (minor defect)	ψ>0.25	0	

3.1.4 Black tape & Mara tape

<u> </u>	.4 black tape & Mara tape		
	FPC or H/S black tape	1.shift spec:	
	shift	1)glue to the polarize	
		[Re	eject]
1		2) IC bare [Re	eject]
'	(minor defect)	2. left-and-right spec:	
		1) exceed of FPC edge or I	H-S
		edge [R	eject]
		2)IC bare [F	Reject]
2	No black tape	No black tape	
	(Major defect)	[Re	eject]
3	Tape position mistake	Not by engineering drawing	
3	(minor defect)	[Re	eject]
4	Mara tape defect	Peel before pulling the pro-	tecting
		film.	
	(minor defect)	[Re	eject]

3.1.5 Silicon and Tuffy glue

No	Defect item	Criteria		Rem	nark	
	Quantity of silicon	Uncover the ITO and circuit area.	note:	compared	by	engineering
١.	(minor defect)	[Reject]	drawi	ng.		
1						

No	Defect item	Criteria	Remark
2	Tuffy glue (minor defect)	<ol> <li>Uncover the reveal copper area [Reject]</li> <li>Cover layer 0.3mm(Min) ~ 3.0mm(Max) [accept]</li> </ol>	requirement, refer to the
3	Depth of glue covering (minor defect)	Depth of glue covering overtop front Polarizer 【Reject】	Except of the special requirement

#### 3.2 Electrical criteria

No	Defect item	Criteria	Remark
1	No display	No display	
Ľ	(Major defect)	【Reject】	
2	Missing line	Missing line	
	(Major defect)	【Reject】	
3	Seg-com light and dark	Seg-com light and dark	ND filter 2% test
	(Major defect)	[Reject]	
4	No display in immobility	No display in immobility	
4	(Major defect)	【Reject】	
5	Flicker of Pattern	Flicker of Pattern	
5	(Major defect)	[Reject]	
6	Mura (Major defect)	ND filter 2% test	
7	Over current	Over current	
	(Major defect)	[Reject]	
	Voltage out of specification	Voltage out of specification	
8		[Reject]	
	(Major defect)		
	Pattern blur ,error code	Pattern blur ,error code	
9		[Reject]	
	(Major defect)		
	Dark light, Flicker	Dark light, Flicker	
10	(Major defect)	[Reject]	

No	Defect item	Criteria		Remark
	Black/White dots Dirty dots eyewinker	Specification	Allowable	Note1: disregard if out of
	· Dirty dots · eyewirker	ψ≦0.15mm	disregard	AA 
11	(Major defeat)	$0.15 mm <\!$	2	$\phi$
	(Major defect)	$0.25 \text{mm} {<} \psi \leqq 0.3 \text{mm}$	1	ψ
		0.3mm<ψ	0	
	Fiber · glass cratch ·	W≦0.03mm	disregard	note1: L: Length , W: Width
10	polarizer scratch/folded	0.03mm <w≦0.05mm; L≦3.0mm</w≦0.05mm; 	2	note2: disregard if out of AA
12	(minor defect)	0.05mm <w≦0.1mm; L≦3.0mm</w≦0.1mm; 	1	
		W>0.1mm ; L>3.0mm	0	,

#### 12. Precautions For Using LCD Modules

Please pay attentions to the followings as using the LCD module.

#### 12.1 Handling

- (a) Do not apply strong mechanical stress like drop, shock or any force to LCD module. It may cause improper operation, even damage.
- (b) Because the ITO film very fragile and easy to be damaged, do not hit, press or rub the display surface with hard materials.
- (c) Do not put heavy or hard material on the display surface, and do not stack LCD modules.
- (d) If the display surface is dirty, please wipe the surface softly with cotton swab or clean cloth.
- (e) Wipe off water droplets or oil immediately.
- (f) Protect the LCD module from ESD. It will damage the LSI and the electronic circuit.
- (g) Do not touch the output pins directly with bare hands.
- (h) Do not disassemble the LCD module.

#### 12.2 Storage

- (a) Do not leave the LCD modules in high temperature, especially in high humidity for a long time.
- (b) Do not expose the LCD modules to sunlight directly.
- (c) The liquid crystal is deteriorated by ultraviolet. Do not leave it in strong ultraviolet ray for a long time.
- (d) Avoid condensation of water. It may cause improper operation.
- (e) Please stack only up to the number stated on carton box for storage and transportation. Excessive weight will cause deformation and damage of carton box.

#### 12.3 Operation

- (a) When mounting or dismounting the LCD modules, turn the power off.
- (b) Protect the LCD modules from electric shock.
- (c) The Driver IC control algorithms stated above should always obeyed to avoid damaging the LSI and electronic circuit.
- (d) Be careful to avoid mixing up the polarity of power supply for backlight.

- (e) Absolute maximum rating specified above has to be always kept in any case. Exceeding it may cause non-recoverable damage of electronic components or, nevertheless, burning.
- (f) When a static image is displayed for a long time, remnant image is likely to occur.
- (g) Be sure to avoid bending the FPC to an acute shape, it might break FPC.

#### 12.4 Others

- (a) If the liquid crystal leaks from the panel, it should be kept away from the eyes or mouth.
- (b) It is recommended to peel off the protection film on the ITO film slowly so that the electrostatic charge can be minimized.
- (c) It is recommended to peel off the protection film on the polarizer slowly so that the electrostatic charge can be minimized.

### 13. Records Of Version

Version	Revise Date	Page	Content
0.0	2008-10-7	All	First Issue