



NTC thermistors for inrush current limiting

Leaded and coated disks

Series/Type: B57153
Date: March 2006

Applications

- Switch-mode power supplies

Features

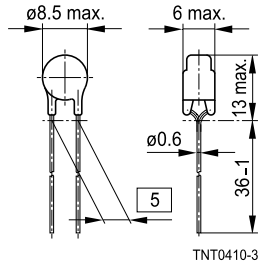
- Useable in series connections up to 265 V_{rms}
- Coated thermistor disk
- Cost-effective NTC for low-power applications
- Kinked leads of tinned copper wire
- Small size
- UL approval (E69802)

Options

Resistance tolerance <20% and alternative lead configurations available on request

Delivery mode

Bulk (standard),
cardboard tape, reeled or in Ammo pack

Dimensional drawing


Dimensions in mm
Approx. weight 0.6 g

General technical data

Climatic category	(IEC 60068-1)	P_{max}	55/170/21	
Max. power	(at 25 °C)	$\Delta R_R/R_R$	±20	W
Resistance tolerance		T_R	25	°C
Rated temperature		$\Delta B/B$	±3	%
B value tolerance		δ_{th}	approx. 8	mW/K
Dissipation factor	(in air)	τ_c	approx. 30	s
Thermal cooling time constant	(in air)	C_{th}	approx. 240	mJ/K
Heat capacity				

Electrical specification and ordering codes

R_{25}	I_{max} (0...65 °C)	No. of R/T char- acteristic	$B_{25/100}$	$C_{test}^{(1)}$ 230 V	$C_{test}^{(1)}$ 110 V	Param. for $R(I)^{(1)}$	Param. for $R(I)^{(1)}$	Ordering code
Ω	A		K	μF	μF	k	n	
4.7	3.0	1202	2800	100	400	0.644	-1.30	B57153S0479M000
8	2.2	1202	2800	100	400	0.778	-1.30	B57153S0809M000
10	2.0	1202	2800	100	400	0.838	-1.30	B57153S0100M000
15	1.8	1203	2900	100	400	0.934	-1.32	B57153S0150M000
16	1.7	1203	2900	100	400	0.953	-1.32	B57153S0160M000
33	1.3	1302	3000	100	400	1.18	-1.33	B57153S0330M000

1) For details on the capacitance C_{test} as well as on the parameters k and n refer to "Application Notes", chapters 2.6 and 2.7.

Reliability data

Test	Standard	Test conditions	$\Delta R_{25}/R_{25}$ (typical)	Remarks
Storage in dry heat	IEC 60068-2-2	Storage at upper category temperature T: 170 °C t: 1000 h	< 10%	No visible damage
Storage in damp heat, steady state	IEC 60068-2-78	Temperature of air: 40 °C Relative humidity of air: 93% Duration: 21 days	< 5%	No visible damage
Rapid temperature cycling	IEC 60068-2-14	Lower test temperature: -55 °C Upper test temperature: 170 °C Number of cycles: 10	< 10%	No visible damage
Endurance		$I = I_{\max}$ t: 1000 h	< 10%	No visible damage
Cyclic endurance		$I = I_{\max}$, 1000 cycles On-time = 1 min Cooling time = 6 min	< 10%	No visible damage
Transient load		Capacitance = C_{test} Number of cycles: 1000	< 5%	No visible damage

Cautions and warnings for ICLs

The self-heating of a thermistor during operation depends on the load applied and the applicable dissipation factor.

When loaded with maximum allowable current/power and the specified dissipation factor is taken as a basis, the NTC thermistor may reach a mean temperature of up to 250 °C.

The heat developed during operation will also be dissipated through the lead wires. So the contact areas, too, may become quite hot at maximum load.

When mounting NTC thermistors you have to ensure that there is an adequate distance between the thermistor and all parts which are sensitive to heat or combustible.

Cautions and warnings

General

See "Important notes" at the end of this document.

Storage

- Store thermistors only in original packaging. Do not open the package before storage.
- Storage conditions in original packaging: storage temperature $-25\text{ }^{\circ}\text{C} \dots +45\text{ }^{\circ}\text{C}$, relative humidity $\leq 75\%$ annual mean, maximum 95%, dew precipitation is inadmissible.
- Do not store SMDs where they are exposed to heat or direct sunlight. Otherwise, the packing material may be deformed or SMDs may stick together, causing problems during mounting.
- Avoid contamination of thermistors surface during storage, handling and processing.
- Avoid storage of thermistor in harmful environments like corrosive gases (SO_x, Cl etc).
- After opening the factory seals, such as polyvinyl-sealed packages, use the SMDs as soon as possible.
- Solder thermistors after shipment from EPCOS within the time specified:
SMDs: 12 months
Leaded components: 24 months

Handling

- NTC thermistors must not be dropped. Chip-offs must not be caused during handling of NTCs.
- Components must not be touched with bare hands. Gloves are recommended.
- Avoid contamination of thermistor surface during handling.

Soldering

- Use resin-type flux or non-activated flux.
- Insufficient preheating may cause ceramic cracks.
- Rapid cooling by dipping in solvent is not recommended.
- Complete removal of flux is recommended.

Mounting

- When NTC thermistors are encapsulated with sealing material or overmolded with plastic material, the precautions given in chapter "Mounting instructions", "Sealing, potting and overmolding" must be observed.
- Electrode must not be scratched before/during/after the mounting process.
- Contacts and housings used for assembly with thermistor have to be clean before mounting.
- During operation, the thermistor's surface temperature can be very high (ICL). Ensure that adjacent components are placed at a sufficient distance from the thermistor to allow for proper cooling of the thermistors.
- Ensure that adjacent materials are designed for operation at temperatures comparable to the surface temperature of the thermistor. Be sure that surrounding parts and materials can withstand this temperature.
- Make sure that thermistors (ICLs) are adequately ventilated to avoid overheating.
- Avoid contamination of thermistor surface during processing.

Operation

- Use thermistors only within the specified operating temperature range.
- Use thermistors only within the specified voltage and current ranges (ICLs).
- Environmental conditions must not harm the thermistors. Use thermistors only in normal atmospheric conditions.
- Contact of NTC thermistors with any liquids and solvents should be prevented. It must be ensured that no water enters the NTC thermistor (e.g. through plug terminals). For measurement purposes (checking the specified resistance vs. temperature), the component must not be immersed in water but in suitable liquids (e.g. Galden).
- Avoid dewing and condensation.
- Be sure to provide an appropriate fail-safe function to prevent secondary product damage caused by malfunction (e.g. use VDR for limitation of overvoltage condition).

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