



AVR-IO development board

Net	Pin
	1
	2

Net	Pin
	1
	2

Net	Pin
	1
	2

Net	Pin
	1
	2

X7 OUT1	
Pin	Net
1	
2	
3	

X8 OUT2	
Pin	Net
1	
2	
3	

X9 OUT3	
Pin	Net
1	
2	
3	

X10 OUT4	
Pin	Net
1	
2	
3	